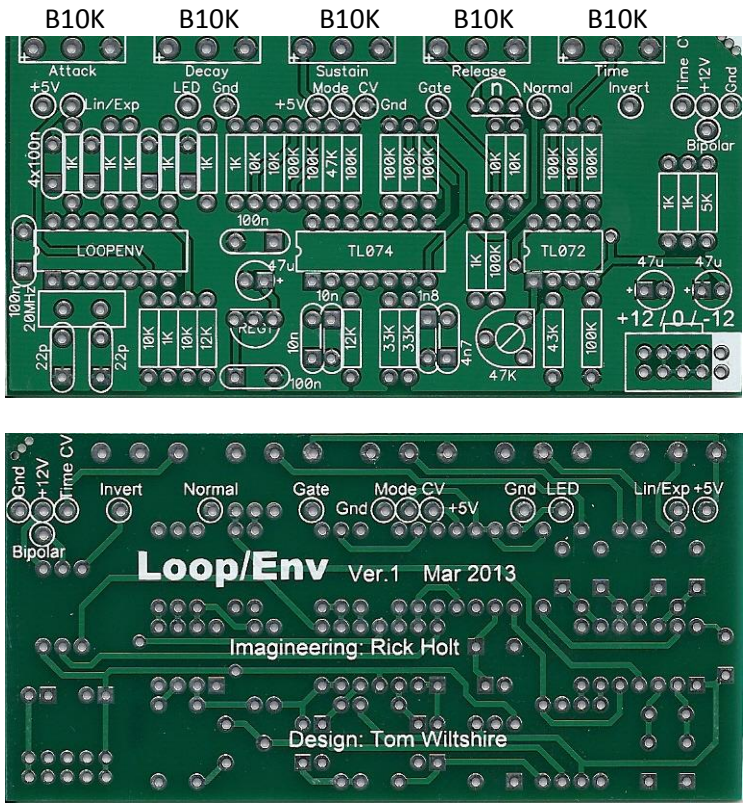
FREQUENCY CENTRAL

Build documentation for:

LOOP/ENV GENERATOR

Powered by Electric Druid code



Bill of Materials

1K x 10 5K (4.7K will do)* x 1 10K x 6 12K x 2 33K x 2 43K (47K will do)* x 1 47K x 1 100K x 11	22pF x 2 1.8nF (or 2.2nF)* x 1 4.7nF x 1 10nF x 2 100nF x 7 47uF x 3	LOOPENV PIC x 1 TL074 (or TL084) x 1 BC547 x 1 78L05 (REG1 on PCB) x 1 20 MHz crystal x 1 5mm red RED + bezel	Alpha B10K (16mm) x 5 47K trimmer x 1 (50K will do)* SPDT toggle on/on x 1 (Lin/Exp) SPDT toggle on/off/on x 1 (ADSR/Loop)

*These are recommended values, but quite unusual, it's perfectly ok to use the part subs suggested.

Please observe correct polarity of the electrolytic caps, voltage regulators, transistor, ICs etc!

Trimmer: Set the Time pot fully clockwise, adjust trimmer until you measure 5V at pin 8 of LOOPENV PIC.

Image showing connection of Time CV socket (far right).

- Input tab to **Time** pad on PCB
- Normalled tab to **+12V** pad on PCB
- Ground tab to **Gnd** pad on PCB

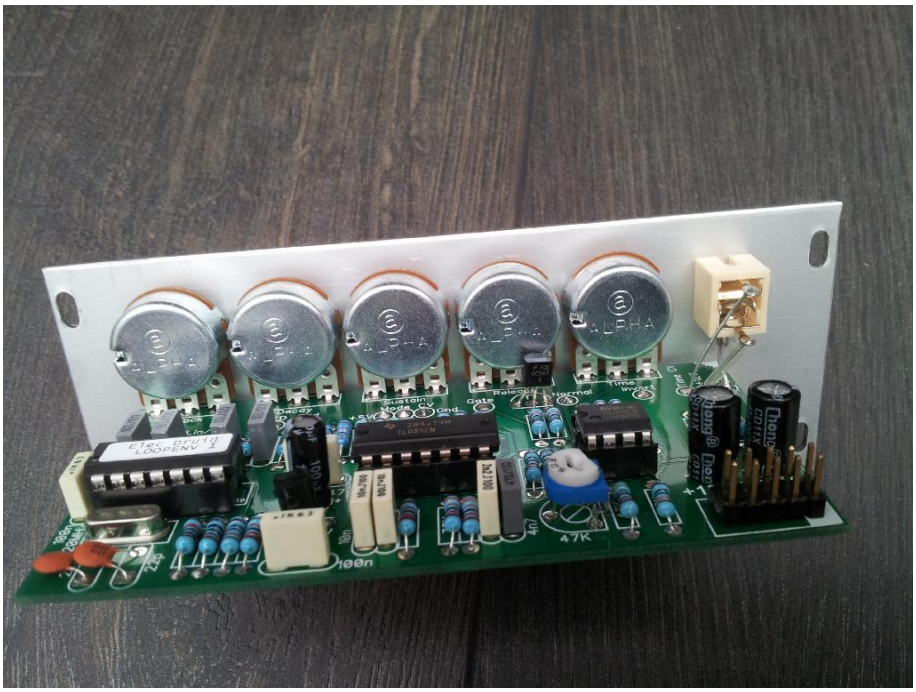
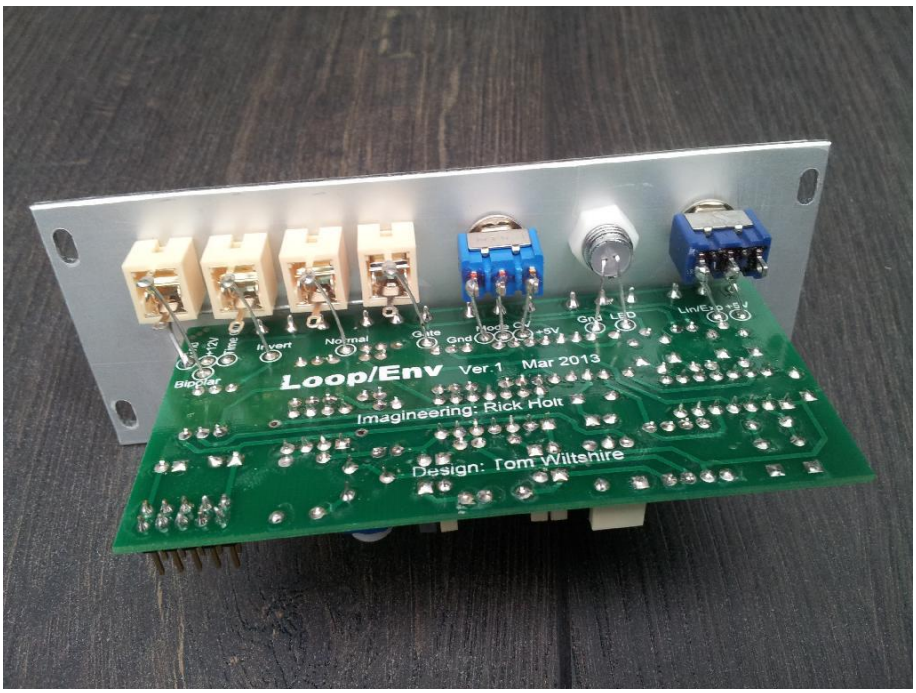


Image showing connection of (left to right): socket ground tabs, Bipolar out, Invert out, Normal out, Gate in, Mode switch, LED, Lin/Exp switch.



RDH 01/12/13

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